IFU1	**IFU2**	**IFU3**	**ID1**	**ID2**	**RAT**	**ROB**	**DIS**	**EX**	**RET1**	**RET2**

IFU = Instruction Fetch Unit
ID = Instruction Decode
RAT = Register Allocator
ROB = Reorder Buffer
DIS = Dispatcher
EX = Execute Stage
RET = Retire Unit

Figure 13.7 The Pentium II Pipeline